

10/507321

DT15 Rec'd PCT/PTO 1.0 SEP 2004

VERIFICATION

The undersigned, of the below address, hereby certifies that he/she well knows both the English and Japanese languages, and that the attached is an accurate English translation of the PCT application filed on March 6, 2003 under No. PCT/JP03/02669.

The undersigned declares further that all statements made herein of his/her own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signed this 10th day of September, 2004.

Signature:


Name: Shiro TERASAKI

Address: c/o Soei Patent & Law Firm Ginza First Bldg.,
10-6, Ginza 1-chome, Chuo-ku, Tokyo 104-0061
Japan

Best Available Copy

21/PRTS

10/507321

DT15 Rec'd PCT/PTO 1.0 SEP 2004

FP03-0042-00

DESCRIPTION

Substrate Dividing Method

Technical Field

5 The present invention relates to a substrate dividing method used for dividing a substrate such as a semiconductor substrate in a step of making a semiconductor device or the like.

Background Art

10 As semiconductor devices have been becoming smaller in recent years, there are cases where a semiconductor substrate is thinned to a thickness of several tens of micrometers in a step of making a semiconductor device. When thus thinned semiconductor substrate is cut and divided by a blade, chipping and cracking occur more than in the case
15 where a semiconductor substrate is thicker, thereby causing a problem that the yield of semiconductor chips obtained by dividing the semiconductor substrate decreases.

20 Known as semiconductor substrate dividing methods which can solve such a problem are those described in Japanese Patent Application Laid-Open Nos. SHO 64-38209 and SHO 62-4341.

25 In the methods described in these publications, a semiconductor substrate having a front face formed with a functional device is inscribed with a groove by a blade on the front face side, then an adhesive sheet is attached to the front face, so as to hold the semiconductor substrate,

FP03-0042-00

and the rear face of the semiconductor substrate is ground until the groove formed beforehand is exposed, thereby thinning the semiconductor substrate and dividing the semiconductor substrate.

5 **Disclosure of the Invention**

If the grinding of the rear face of the semiconductor substrate is performed by surface grinding in the methods described in the above-mentioned publications, however, chipping and cracking may occur at side faces of the groove formed beforehand in the semiconductor substrate when the surface-ground face reaches the groove.

In view of such a circumstance, it is an object of the present invention to provide a substrate dividing method which can prevent chipping and cracking from occurring, and thin and divide a substrate.

For achieving the above-mentioned object, the substrate dividing method in accordance with the present invention comprises the steps of irradiating a substrate with laser light while positioning a light-converging point within the substrate, so as to form a modified region due to multiphoton absorption within the substrate, and causing the modified region to form a starting point region for cutting along a line along which the substrate should be cut in the substrate inside by a predetermined distance from a laser light incident face of the substrate; and grinding the substrate after the step of forming the starting point region

FP03-0042-00

for cutting such that the substrate attains a predetermined thickness.

5 Since this substrate dividing method irradiates the substrate with laser light while positioning a light-converging point within the substrate in the step of forming a starting point region for cutting, so as to generate a phenomenon of multiphoton absorption within the substrate, thereby forming a modified region, this modified region can form a starting point region for cutting within the substrate
10 along a desirable line along which the substrate should be cut for cutting the substrate. When a starting point region for cutting is formed within the substrate, a fracture is generated in the substrate in its thickness direction from the starting point region for cutting acting as a start point
15 naturally or with a relatively small force exerted thereon.

In the step of grinding the substrate, the substrate is ground such that the substrate attains a predetermined thickness after the starting point region for cutting is formed within the substrate. Here, even when the ground
20 surface reaches the fracture generated from the starting point region for cutting acting as a start point, cut surfaces of the substrate cut by the fracture remain in close contact with each other, whereby the substrate can be prevented from chipping and cracking upon grinding.

25 This can prevent chipping and cracking from occurring, and can thin and divide the substrate.

FP03-0042-00

Here, the light-converging point refers to a location at which laser light is converged. The grinding encompasses shaving, polishing, chemical etching, and the like. The starting point region for cutting refers to a region to become a start point for cutting when the substrate is cut. Therefore, the starting point region for cutting is a part to cut where cutting is to be performed in the substrate. The starting point region for cutting may be produced by continuously forming a modified region or intermittently forming a modified region.

The substrate encompasses semiconductor substrates such as silicon substrates and GaAs substrates, and insulating substrates such as sapphire substrates and AlN substrates. When the substrate is a semiconductor substrate, an example of the modified region is a molten processed region.

Preferably, a front face of the substrate is formed with a functional device, and a rear face of the substrate is ground in the step of grinding the substrate. Since the substrate can be ground after forming the functional device, a chip thinned so as to conform to a smaller size of a semiconductor device, for example, can be obtained. Here, the functional device refers to light-receiving devices such as photodiodes, light-emitting devices such as laser diodes, circuit devices formed as circuits, etc.

Preferably, the step of grinding the substrate includes a step of subjecting the rear face of the substrate to chemical

FP03-0042-00

etching. When the rear face of the substrate is subjected to chemical etching, the rear face of the substrate becomes smoother as a matter of course. Also, since the cut surfaces of the substrate cut by the fracture generated from the starting point region for cutting acting as a start point remain in close contact with each other, only edge parts on the rear face of the cut surfaces are selectively etched, so as to be chamfered. This can improve the transverse rupture strength of chips obtained by dividing the substrate, and prevent chipping and cracking from occurring in the chips.

Brief Description of the Drawings

Fig. 1 is a plan view of an object to be processed during laser processing in the laser processing method in accordance with an embodiment of the present invention;

Fig. 2 is a sectional view of the object to be processed taken along the line II-II of Fig. 1;

Fig. 3 is a plan view of the object to be processed after laser processing by the laser processing method in accordance with the embodiment;

Fig. 4 is a sectional view of the object to be processed taken along the line IV-IV of Fig. 3;

Fig. 5 is a sectional view of the object to be processed taken along the line V-V of Fig. 3;

Fig. 6 is a plan view of the object to be processed cut by the laser processing method in accordance with the embodiment;

FP03-0042-00

Fig. 7 is a graph showing relationships between the electric field intensity and crack spot size in the laser processing method in accordance with the embodiment;

5 Fig. 8 is a sectional view of the object to be processed in a first step of the laser processing method in accordance with the embodiment;

Fig. 9 is a sectional view of the object to be processed in a second step of the laser processing method in accordance with the embodiment;

10 Fig. 10 is a sectional view of the object to be processed in a third step of the laser processing method in accordance with the embodiment;

15 Fig. 11 is a sectional view of the object to be processed in a fourth step of the laser processing method in accordance with the embodiment;

Fig. 12 is a view showing a photograph of a cut section in a part of a silicon wafer cut by the laser processing method in accordance with the embodiment;

20 Fig. 13 is a graph showing relationships between the laser light wavelength and the internal transmittance of a silicon substrate in the laser processing method in accordance with the embodiment;

Fig. 14 is a schematic diagram of the laser processing apparatus in accordance with Example 1;

25 Fig. 15 is a flowchart for explaining the laser processing method in accordance with Example 1;

FP03-0042-00

Fig. 16 is a view showing the semiconductor substrate after a step of forming a starting point region for cutting in accordance with Example 1;

5 Fig. 17 is a view for explaining a step of attaching a protective film in accordance with Example 1;

Fig. 18 is a view for explaining a step of grinding the semiconductor substrate in accordance with Example 1;

Fig. 19 is a view for explaining a step of attaching an expansion film in accordance with Example 1;

10 Fig. 20 is a view for explaining a step of peeling the protective film in accordance with Example 1;

Fig. 21 is a view for explaining a step of expanding the expansion film and picking up semiconductor chips in accordance with Example 1;

15 Fig. 22 is a view showing chamfers formed at edge parts on the rear face side of cut surfaces of semiconductor chips after the step of grinding the semiconductor substrate in accordance with Example 1;

20 Fig. 23A is a view for explaining a case where a molten processed region remains in a cut surface of a semiconductor chip after the step of grinding the semiconductor substrate in accordance with Example 1, while a fracture reaches the front face before the step of grinding the semiconductor substrate;

25 Fig. 23B is a view for explaining a case where a molten processed region remains in a cut surface of a semiconductor

FP03-0042-00

chip after the step of grinding the semiconductor substrate in accordance with Example 1, while a fracture does not reach the front face before the step of grinding the semiconductor substrate;

5 Fig. 24A is a view for explaining a case where a molten processed region does not remain in a cut surface of a semiconductor chip after the step of grinding the semiconductor substrate in accordance with Example 1, while a fracture reaches the front face before the step of grinding
10 the semiconductor substrate;

 Fig. 24B is a view for explaining a case where a molten processed region does not remain in a cut surface of a semiconductor chip after the step of grinding the semiconductor substrate in accordance with Example 1, while
15 a fracture does not reach the front face before the step of grinding the semiconductor substrate;

 Fig. 25A is a view for explaining a case where a molten processed region remains in an edge part on the rear face side of a cut surface of a semiconductor chip after the step
20 of grinding the semiconductor substrate in accordance with Example 1, while a fracture reaches the front face before the step of grinding the semiconductor substrate;

 Fig. 25B is a view for explaining a case where a molten processed region remains in an edge part on the rear face
25 side of a cut surface of a semiconductor chip after the step of grinding the semiconductor substrate in accordance with

FP03-0042-00

Example 1, while a fracture does not reach the front face before the step of grinding the semiconductor substrate;

Fig. 26A is a sectional view of a marginal part of the semiconductor substrate before the step of grinding the semiconductor substrate in accordance with Example 1;

Fig. 26B is a sectional view of the marginal part of the semiconductor substrate after the step of grinding the semiconductor substrate in accordance with Example 1;

Fig. 27 is a plan view of the sapphire substrate in accordance with Example 2;

Fig. 28 is a sectional view for explaining a step of forming a starting point region for cutting in accordance with Example 2;

Fig. 29 is a sectional view for explaining a step of forming a functional device in accordance with Example 2;

Fig. 30 is a sectional view for explaining a step of attaching a protective film in accordance with Example 2;

Fig. 31 is a sectional view for explaining a step of grinding the sapphire substrate in accordance with Example 2;

Fig. 32 is a sectional view for explaining a step of attaching an expansion film in accordance with Example 2;

Fig. 33 is a sectional view for explaining a step of irradiating the protective film with UV rays in accordance with Example 2;

Fig. 34 is a sectional view for explaining a step of

FP03-0042-00

peeling the protective film in accordance with Example 2;
and

Fig. 35 is a sectional view for explaining a step of
expanding the expansion film and separating semiconductor
5 chips in accordance with Example 2.

Best Modes for Carrying Out the Invention

In the following, a preferred embodiment of the present
invention will be explained in detail with reference to
drawings. The substrate dividing method in accordance with
10 this method comprises the steps of irradiating a substrate
with laser light while positioning a light-converging point
within the substrate, so as to form a modified region due
to multiphoton absorption within the substrate, thereby
forming a starting point region for cutting; and then grinding
15 the substrate such that the substrate attains a predetermined
thickness.

First, a laser processing method carried out in the
step of forming the starting point region for cutting,
multiphoton absorption in particular, will be explained.

20 A material becomes optically transparent if its
absorption bandgap E_G is greater than a photon energy $h\nu$.
Hence, the condition under which absorption occurs in the
material is $h\nu > E_G$. However, even when optically transparent,
the material yields absorption under the condition of $n h\nu$
25 $> E_G$ ($n = 2, 3, 4, \dots$) if the intensity of laser light is
very high. This phenomenon is known as multiphoton

FP03-0042-00

absorption. In the case of pulse waves, the intensity of laser light is determined by the peak power density (W/cm^2) of laser light at a light-converging point thereof. The multiphoton absorption occurs, for example, at a peak power density (W/cm^2) of 1×10^8 (W/cm^2) or higher. The peak power density is determined by (energy per pulse of laser light at the light-converging point)/(laser light beam spot cross-sectional area \times pulse width). In the case of a continuous wave, the intensity of laser light is determined by the electric field strength (W/cm^2) of laser light at the light-converging point.

The principle of laser processing in accordance with the embodiment utilizing such multiphoton absorption will now be explained with reference to Figs. 1 to 6. Fig. 1 is a plan view of a substrate 1 during laser processing; Fig. 2 is a sectional view of the substrate 1 taken along the line II-II of Fig. 1; Fig. 3 is a plan view of the substrate 1 after laser processing; Fig. 4 is a sectional view of the substrate 1 taken along the line IV-IV of Fig. 3; Fig. 5 is a sectional view of the substrate 1 taken along the line V-V of Fig. 3; and Fig. 6 is a plan view of the cut substrate 1.

As shown in Figs. 1 and 2, the front face 3 of the substrate 1 has a desirable line along which the substrate should be cut 5 for cutting the substrate 1. The line along which the substrate should be cut 5 is a linearly extending

FP03-0042-00

virtual line (the substrate 1 may also be formed with an actual line acting as the line along which the substrate should be cut 5). In the laser processing in accordance with this embodiment, the substrate 1 is irradiated with laser light L such that a light-converging point P is positioned within the semiconductor substrate 1 under a condition causing multiphoton absorption, so as to form a modified region 7. Here, the light-converging point is a location where the laser light L is converged.

The laser light L is relatively moved along the line along which the substrate should be cut 5 (in the direction of arrow A), so as to move the light-converging point P along the line along which the substrate should be cut 5. This forms the modified region 7 along the line along which the substrate should be cut 5 only within the substrate 1 as shown in Figs. 3 to 5, and the modified region 7 forms a starting point region for cutting (part to cut) 8. In the laser processing method in accordance with this embodiment, no modified region 7 is formed upon heating the substrate 1 by causing the substrate 1 to absorb the laser light L. Instead, the laser light L is transmitted through the semiconductor substrate 1, so as to generate multiphoton absorption within the semiconductor substrate 1, thereby forming the modified region 7. Hence, the laser light L is hardly absorbed by the front face 3 of the semiconductor substrate 1, whereby the front face 3 of the semiconductor

FP03-0042-00

substrate 1 does not melt.

If a start point exists at a location to cut when cutting the substrate 1, the substrate 1 fractures from this start point and thus can be cut with a relatively small force as shown in Fig. 6. This makes it possible to cut the substrate 1 without generating unnecessary fractures in the front face 3 of the substrate 1.

There seem to be the following two ways of cutting the substrate from the starting point region for cutting acting as a start point. The first case is where, after forming the starting point region for cutting, an artificial force is applied to the substrate, so that the substrate fractures from the starting point region for cutting acting as a start point, whereby the substrate is cut. This is the cutting in the case where the substrate has a large thickness, for example. The application of an artificial force encompasses application of bending stress and shearing stress along the starting point region for cutting of the substrate, and exertion of a temperature difference upon the substrate to generate thermal stress, for example. The other case is where a starting point region for cutting is formed, so that the substrate is naturally fractured in a cross-sectional direction (thickness direction) of the substrate from the starting point region for cutting acting as a start point, whereby the substrate is cut. This is enabled, for example, by forming the starting point region for cutting by a single

FP03-0042-00

row of modified regions when the substrate has a small thickness, and by a plurality of rows of modified regions aligned in the thickness direction when the substrate has a large thickness. Even in the case of natural fracturing, fractures do not extend to the front face at a location not formed with the starting point region for cutting in the part to cut, whereby only the part corresponding to the location formed with the starting point region for cutting can be fractured. Thus, fracturing can be regulated well. Such a fracturing method with favorable controllability is quite effective, since semiconductor substrates such as silicon wafers have recently been apt to become thinner.

The modified region formed by multiphoton absorption in this embodiment includes the following cases (1) to (3):

(1) Case where the modified region is a crack region including one or a plurality of cracks

A substrate (e.g., glass or a piezoelectric material made of LiTaO_3) is irradiated with laser light while a light-converging point is positioned therewithin under a condition with an electric field intensity of at least 1×10^8 (W/cm²) at the light-converging point and a pulse width of 1 μs or less. This pulse width is a condition under which a crack region can be formed only within the substrate while generating multiphoton absorption without causing unnecessary damages to the substrate. This generates a phenomenon of optical damage due to multiphoton absorption

FP03-0042-00

within the substrate. This optical damage induces thermal distortion within the substrate, thereby forming a crack region therewithin. The upper limit of electric field intensity is 1×10^{12} (W/cm²), for example. The pulse width is preferably 1 ns to 200 ns, for example. The forming of a crack region due to multiphoton absorption is described, for example, in "Internal Marking of Glass Substrate by Solid-state Laser Harmonics", Proceedings of 45th Laser Materials Processing Conference (December 1998), pp. 23-28.

10 The inventors determined relationships between the electric field intensity and the magnitude of crack by an experiment. Conditions for the experiment are as follows:

(A) Substrate: Pyrex (registered trademark) glass (having a thickness of 700 μ m)

15 (B) Laser

Light source: semiconductor laser pumping Nd:YAG laser

Wavelength: 1064 nm

20 Laser light spot cross-sectional area: 3.14×10^{-8} cm²

Oscillation mode: Q-switch pulse

Repetition frequency: 100 kHz

Pulse width: 30 ns

Output: output < 1 mJ/pulse

25 Laser light quality: TEM₀₀

Polarization characteristic: linear

FP03-0042-00

polarization

(C) Light-converging lens

Transmittance with respect to laser light
wavelength: 60%

5 (D) Moving speed of a mounting table mounting the
substrate: 100 mm/sec

Here, the laser light quality being TEM_{00} indicates
that the light convergence is so high that light can be
converged up to about the wavelength of laser light.

10 Fig. 7 is a graph showing the results of the
above-mentioned experiment. The abscissa indicates peak
power density. Since laser light is pulse laser light, its
electric field intensity is represented by the peak power
density. The ordinate indicates the size of a crack part
15 (crack spot) formed within the substrate processed by one
pulse of laser light. Crack spots gather, so as to form a
crack region. The size of a crack spot refers to that of
the part of dimensions of the crack spot yielding the maximum
length. The data indicated by black circles in the graph
20 refers to a case where the light-converging lens (C) has
a magnification of x100 and a numerical aperture (NA) of
0.80. On the other hand, the data indicated by white circles
in the graph refers to a case where the light-converging
lens (C) has a magnification of x50 and a numerical aperture
25 (NA) of 0.55. It is seen that crack spots begin to occur
within the substrate when the peak power density reaches

FP03-0042-00

about 10^{11} (W/cm²), and become greater as the peak power density increases.

A mechanism by which the substrate is cut upon formation of a crack region in the laser processing in accordance with this embodiment will now be explained with reference to Figs. 8 to 11. As shown in Fig. 8, the substrate 1 is irradiated with laser light L while positioning the light-converging point P within the substrate 1 under a condition where multiphoton absorption occurs, so as to form a crack region 9 therewithin along a line along which the substrate should be cut. The crack region 9 is a region including one or a plurality of crack spots. The crack region 9 forms a starting point region for cutting. As shown in Fig. 9, the crack further grows while using the crack region 9 as a start point (i.e., using the starting point region for cutting as a start point). As shown in Fig. 10, the crack reaches the front face 3 and rear face 21 of the substrate 1. As shown in Fig. 11, the substrate 1 breaks, so as to be cut. The crack reaching the front face and rear face of the substrate may grow naturally or grow as a force is applied to the substrate.

(2) Case where the modified region is a molten processed region

A substrate (e.g., a semiconductor material such as silicon) is irradiated with laser light while a light-converging point is positioned therewithin under a condition with an electric field intensity of at least 1

FP03-0042-00

$\times 10^8$ (W/cm²) at the light-converging point and a pulse width of 1 μ s or less. As a consequence, the inside of the substrate is locally heated by multiphoton absorption. This heating forms a molten processed region within the substrate. The

5 molten processed region refers to a region once melted and then re-solidified, a region just in a melted state, or a region in the process of re-solidifying from its melted state, and may also be defined as a phase-changed region or a region having changed its crystal structure. The molten processed

10 region may also be regarded as a region in which a certain structure has changed into another structure in monocrystal, amorphous, and polycrystal structures. Namely, it refers to a region in which a monocrystal structure has changed

15 into an amorphous structure, a region in which a monocrystal structure has changed into a polycrystal structure, and a region in which a monocrystal structure has changed into a structure including an amorphous structure and a polycrystal structure, for example. When the substrate is a silicon monocrystal structure, the molten processed region

20 is an amorphous silicon structure, for example. The upper limit of electric field intensity is 1×10^{12} (W/cm²), for example. The pulse width is preferably 1 ns to 200 ns, for example.

By an experiment, the inventors have verified that a

25 molten processed region is formed within a silicon wafer. Conditions for the experiment are as follows:

FP03-0042-00

(A) Substrate: silicon wafer (having a thickness of 350 μm and an outer diameter of 4 inches)

(B) Laser

Light source: semiconductor laser pumping

5 Nd:YAG laser

Wavelength: 1064 nm

Laser light spot cross-sectional area: $3.14 \times 10^{-8} \text{ cm}^2$

Oscillation mode: Q-switch pulse

10 Repetition frequency: 100 kHz

Pulse width: 30 ns

Output: 20 $\mu\text{J}/\text{pulse}$

Laser light quality: TEM_{00}

Polarization characteristic: linear

15 polarization

(C) Light-converging lens

Magnification: x50

N. A.: 0.55

20 Transmittance with respect to laser light wavelength: 60%

(D) Moving speed of a mounting table mounting the substrate: 100 mm/sec

25 Fig. 12 is a view showing a photograph of a cut section in a part of a silicon wafer cut by laser processing under the above-mentioned conditions. A molten processed region 13 is formed within a silicon wafer 11. The size of the molten

FP03-0042-00

processed region 13 formed under the above-mentioned conditions is about 100 μm in the thickness direction.

The fact that the molten processed region 13 is formed by multiphoton absorption will now be explained. Fig. 13 is a graph showing relationships between the wavelength of laser light and the transmittance within the silicon substrate. Here, respective reflecting components on the front face side and rear face side of the silicon substrate are eliminated, whereby only the transmittance therewithin is represented. The above-mentioned relationships are shown in the cases where the thickness t of the silicon substrate is 50 μm , 100 μm , 200 μm , 500 μm , and 1000 μm , respectively.

For example, it is seen that laser light transmits through the silicon substrate by at least 80% at 1064 nm, where the wavelength of Nd:YAG laser is located, when the silicon substrate has a thickness of 500 μm or less. Since the silicon wafer 11 shown in Fig. 12 has a thickness of 350 μm , the molten processed region 13 due to multiphoton absorption is formed near the center of the silicon wafer, i.e., at a part separated from the front face by 175 μm . The transmittance in this case is 90% or greater with reference to a silicon wafer having a thickness of 200 μm , whereby the laser light is absorbed within the silicon wafer 11 only slightly and is substantially transmitted therethrough. This means that the molten processed region 13 is not formed

FP03-0042-00

by laser light absorption within the silicon wafer 11 (i.e.,
not formed upon usual heating with laser light), but by
multiphoton absorption. The forming of a molten processed
region by multiphoton absorption is described, for example,
5 in "Processing Characteristic Evaluation of Silicon by
Picosecond Pulse Laser", Preprints of the National Meeting
of Japan Welding Society, No. 66 (April 2000), pp. 72-73.

Here, a fracture is generated in the cross-sectional
direction while using a molten processed region as a start
10 point, whereby the silicon wafer is cut when the fracture
reaches the front face and rear face of the silicon wafer.
The fracture reaching the front face and rear face of the
silicon wafer may grow naturally or grow as a force is applied
to the silicon wafer. The fracture naturally grows from the
15 starting point region for cutting to the front face and rear
face of the silicon wafer in any of the cases where the fracture
grows from the molten processed region in a melted state
and where the fracture grows from the molten processed region
in the process of re-solidifying from the melted state. In
20 any of these cases, the molten processed region is formed
only within the silicon wafer. In the cut section after
cutting, the molten processed region is formed only
therewithin as shown in Fig. 12. When a molten processed
region is formed within the substrate, unnecessary fractures
25 deviating from a line along which the substrate should be
cut are hard to occur at the time of fracturing, which makes

FP03-0042-00

it easier to control the fracturing.

(3) Case where the modified region is a refractive index change region

A substrate (e.g., glass) is irradiated with laser light while a light-converging point is positioned therewithin under a condition with an electric field intensity of at least 1×10^8 (W/cm²) at the light-converging point and a pulse width of 1 ns or less. When multiphoton absorption is generated within the substrate with a very short pulse width, the energy caused by multiphoton absorption is not transformed into thermal energy, so that a permanent structural change such as ionic valence change, crystallization, or polarization orientation is induced within the substrate, whereby a refractive index change region is formed. The upper limit of electric field intensity is 1×10^{12} (W/cm²), for example. The pulse width is preferably 1 ns or less, more preferably 1 ps or less, for example. The forming of a refractive index change region by multiphoton absorption is described, for example, in "Formation of Photoinduced Structure within Glass by Femtosecond Laser Irradiation", Proceedings of 42th Laser Materials Processing Conference (November 1997), pp. 105-111.

The cases of (1) to (3) are explained as modified regions formed by multiphoton absorption in the foregoing. When a starting point region for cutting is formed as follows in view of the crystal structure of the substrate, the cleavage

FP03-0042-00

property thereof, and the like, the substrate can be cut with a smaller force and a higher accuracy while using the starting point region for cutting as a start point.

5 Namely, in the case of a substrate made of a monocrystal semiconductor having a diamond structure such as silicon, the starting point region for cutting is preferably formed in a direction along the (111) plane (first cleavage plane) or (110) plane (second cleavage plane). In the case of a substrate made of a III-V family compound semiconductor
10 having a zinc ore type structure such as GaAs, the starting point region for cutting is preferably formed in a direction along the (110) plane. In the case of a substrate having a hexagonal crystal structure such as sapphire (Al_2O_3), a starting point region for cutting is preferably formed in
15 a direction along the (1120) plane (A plane) or (1100) plane (M plane) while using the (0001) plane (C plane) as a principal plane.

 When the substrate is formed with an orientation flat along a direction to be formed with the starting point region
20 for cutting (e.g., in a direction along the (111) plane in the monocrystal silicon substrate) or a direction orthogonal to the direction to be formed with the starting point region for cutting, the starting point region for cutting extending along the direction to be formed with the starting point
25 region for cutting can be formed in the substrate in an easy and accurate manner with reference to the orientation flat.

FP03-0042-00

In the following, the present invention will be explained more specifically with reference to Examples.

Example 1

Example 1 of the substrate dividing method in accordance with the present invention will now be explained. Example 1 is directed to a case where the substrate 1 is a silicon wafer (having a thickness of 350 μm and an outer diameter of 4 inches) ("substrate 1" will hereinafter be referred to as "semiconductor substrate 1" in Example 1), whereas the front face 3 of the semiconductor substrate 1 is formed with a plurality of functional devices in a device manufacturing process.

First, before explaining a step of forming a starting point region for cutting within the semiconductor substrate 1, a laser processing apparatus employed in the step of forming a starting point region for cutting will be explained with reference to Fig. 14. Fig. 14 is a schematic diagram of the laser processing apparatus 100.

The laser processing apparatus 100 comprises a laser light source 101 for generating laser light L; a laser light source controller 102 for controlling the laser light source 101 so as to regulate the output, pulse width, etc. of laser light L and the like; a dichroic mirror 103, arranged so as to change the orientation of the optical axis of laser light L by 90° , having a function of reflecting the laser light L; a light-converging lens 105 for converging the laser

FP03-0042-00

light L reflected by the dichroic mirror 103; a mounting table 107 for mounting a semiconductor substrate 1 irradiated with the laser light L converged by the light-converging lens 105; an X-axis stage 109 for moving the mounting table
5 107 in the X-axis direction; a Y-axis stage 111 for moving the mounting table 107 in the Y-axis direction orthogonal to the X-axis direction; a Z-axis stage 113 for moving the mounting table 107 in the Z-axis direction orthogonal to the X- and Y-axis directions; and a stage controller 115
10 for controlling the movement of these three stages 109, 111, 113.

The Z-axis direction is a direction orthogonal to the front face 3 of the semiconductor substrate 1, and thus becomes the direction of focal depth of laser light L incident on
15 the semiconductor substrate 1. Therefore, moving the Z-axis stage 113 in the Z-axis direction can position the light-converging point P of laser light L within the semiconductor substrate 1. This movement of light-converging point P in X(Y)-axis direction is effected
20 by moving the semiconductor substrate 1 in the X(Y)-axis direction by the X(Y)-axis stage 109 (111).

The laser light source 101 is an Nd:YAG laser generating pulse laser light. Known as other kinds of laser usable as the laser light source 101 include Nd:YVO₄ laser, Nd:YLF laser,
25 and titanium sapphire laser. For forming a molten processed region, Nd:YAG laser, Nd:YVO₄ laser, and Nd:YLF laser are

FP03-0042-00

preferably employed. Though pulse laser light is used for processing the semiconductor substrate 1 in Example 1, continuous wave laser light may be used as long as it can cause multiphoton absorption.

5 The laser processing apparatus 100 further comprises an observation light source 117 for generating a visible light beam for irradiating the semiconductor substrate 1 mounted on the mounting table 107, and a visible light beam splitter 119 disposed on the same optical axis as that of
10 the dichroic mirror 103 and light-converging lens 105. The dichroic mirror 103 is disposed between the beam splitter 119 and light-converging lens 105. The beam splitter 119 has a function of reflecting about a half of a visual light beam and transmitting the remaining half therethrough, and
15 is arranged so as to change the orientation of the optical axis of the visual light beam by 90°. About a half of the visible light beam generated from the observation light source 117 is reflected by the beam splitter 119, and thus reflected visible light beam is transmitted through the
20 dichroic mirror 103 and light-converging lens 105, so as to illuminate the front face 3 of the semiconductor substrate 1 including the line along which the substrate should be cut 5 and the like.

25 The laser processing apparatus 100 further comprises an image pickup device 121 and an imaging lens 123 which are disposed on the same optical axis as that of the beam

FP03-0042-00

splitter 119, dichroic mirror 103, and light-converging lens 105. An example of the image pickup device 121 is a CCD camera. The reflected light of the visual light beam having illuminated the front face 3 including the line along which
5 the substrate should be cut 5 and the like is transmitted through the light-converging lens 105, dichroic mirror 103, and beam splitter 119 and forms an image by way of the imaging lens 123, whereas thus formed image is captured by the image pickup device 121, so as to yield imaging data.

10 The laser processing apparatus 100 further comprises an imaging data processor 125 for inputting the imaging data outputted from the image pickup device 121, an overall controller 127 for controlling the laser processing apparatus 100 as a whole, and a monitor 129. According to the imaging
15 data, the imaging data processor 125 calculates focal point data for positioning the focal point of the visible light generated from the observation light source 117 onto the front face 3. According to the focal point data, the stage controller 115 controls the movement of the Z-axis stage
20 113, so that the focal point of visible light is positioned on the front face 3. Hence, the imaging data processor 125 functions as an autofocus unit. Also, according to the imaging data, the imaging data processor 125 calculates image data such as an enlarged image of the front face 3. The image
25 data is sent to the overall controller 127, subjected to various kinds of processing therein, and then sent to the

FP03-0042-00

monitor 129. As a consequence, an enlarged image or the like is displayed on the monitor 129.

5 Data from the stage controller 115, image data from the imaging data processor 125, and the like are fed into the overall controller 127. According to these data as well, the overall controller 127 regulates the laser light source controller 102, observation light source 117, and stage controller 115, thereby controlling the laser processing apparatus 100 as a whole. Thus, the overall controller 127
10 functions as a computer unit.

With reference to Figs. 14 and 15, a step of forming a starting point region for cutting in the case using the above-mentioned laser processing apparatus 100 will be explained. Fig. 15 is a flowchart for explaining the step
15 of forming a starting point region for cutting.

Light absorption characteristics of the semiconductor substrate 1 are determined by a spectrophotometer or the like which is not depicted. According to the results of measurement, a laser light source 101 generating laser light
20 L having a wavelength to which the semiconductor substrate 1 is transparent or exhibits a low absorption is chosen (S101). Subsequently, the thickness of the semiconductor substrate 1 is measured. According to the result of measurement of thickness and the refractive index of the semiconductor
25 substrate 1, the amount of movement of the semiconductor substrate 1 in the Z-axis direction is determined (S103).

FP03-0042-00

This is an amount of movement of the semiconductor substrate 1 in the Z-axis direction with reference to the light-converging point P of laser light L positioned at the front face 3 of the semiconductor substrate 1 in order for
5 the light-converging point P of laser light L to be positioned within the semiconductor substrate 1. This amount of movement is fed into the overall controller 127.

The semiconductor substrate 1 is mounted on the mounting table 107 of the laser processing apparatus 100.
10 Subsequently, visible light is generated from the observation light source 117, so as to illuminate the semiconductor substrate 1 (S105). The illuminated front face 3 of the semiconductor substrate 1 including the line along which the substrate should be cut 5 is captured by the image pickup
15 device 121. The line along which the substrate should be cut 5 is a desirable virtual line for cutting the semiconductor substrate 1. Here, in order to obtain semiconductor chips by dividing the semiconductor substrate 1 into the functional devices formed on its front face 3, the line along which
20 the substrate should be cut 5 is set like a grid running between the functional devices adjacent each other. The imaging data captured by the imaging device 121 is sent to the imaging data processor 125. According to the imaging data, the imaging data processor 125 calculates such focal
25 point data that the focal point of visible light from the observation light source 117 is positioned at the front face

FP03-0042-00

3 (S107).

The focal point data is sent to the stage controller 115. According to the focal point data, the stage controller 115 moves the Z-axis stage 113 in the Z-axis direction (S109).

5 As a consequence, the focal point of visible light from the observation light source 117 is positioned at the front face 3 of the semiconductor substrate 1. According to the imaging data, the imaging data processor 125 calculates enlarged image data of the front face 3 of the semiconductor substrate
10 1 including the line along which the substrate should be cut 5. The enlarged image data is sent to the monitor 129 by way of the overall controller 127, whereby an enlarged image of the line along which the substrate should be cut 5 and its vicinity is displayed on the monitor 129.

15 Movement amount data determined in step S103 has been fed into the overall controller 127 beforehand, and is sent to the stage controller 115. According to the movement amount data, the stage controller 115 causes the Z-axis stage 113 to move the substrate 1 in the Z-axis direction to a position
20 where the light-converging point P of laser light L is positioned within the semiconductor substrate 1 (S111).

Subsequently, laser light L is generated from the laser light source 101, so as to irradiate the line along which the substrate should be cut 5 in the front face 3 of the
25 semiconductor substrate 1. Then, the X-axis stage 109 and Y-axis stage 111 are moved along the line along which the

FP03-0042-00

substrate should be cut 5, so as to form a molten processed region along the line along which the substrate should be cut 5, thereby forming a starting point region for cutting within the semiconductor substrate 1 along the line along which the substrate should be cut 5 (S113).

The foregoing completes the step of forming a starting point region for cutting, thereby forming the starting point region for cutting within the semiconductor substrate 1. When the starting point region for cutting is formed within the semiconductor substrate 1, a fracture is generated in the thickness direction of the semiconductor substrate 1 from the starting point region for cutting acting as a start point naturally or with a relatively small force exerted thereon.

In Example 1, the starting point region for cutting is formed at a position near the front face 3 side within the semiconductor substrate 1 in the above-mentioned step of forming a starting point region for cutting, and a fracture is generated in the thickness direction of the semiconductor substrate 1 from the starting point region for cutting acting as a start point. Fig. 16 is a view showing the semiconductor substrate 1 after the starting point region for cutting is formed. As shown in Fig. 16, fractures 15 generated from the starting point region for cutting acting as a start point are formed like a grid along lines to cut, and reach only the front face 3 of the semiconductor substrate 1 but not

FP03-0042-00

the rear face 21 thereof. Namely, the fractures 15 generated in the semiconductor substrate 1 separate a plurality of functional devices 19 formed like a matrix on the front face of the semiconductor substrate 1 from each other. The cut surfaces of the semiconductor substrate 1 cut by the fractures 15 are in close contact with each other.

Here, "the starting point region for cutting is formed at a position near the front face 3 side within the semiconductor substrate 1" means that a modified region such as a molten processed region constituting a starting point region for cutting is formed so as to shift from the center position in the thickness direction of the semiconductor substrate 1 (i.e., half thickness position) toward the front face 3. Namely, it refers to a case where the center position of the width of the modified region in the thickness direction of the semiconductor substrate 1 is shifted toward the front face 3 from the center position in the thickness direction of the semiconductor substrate 1, and is not limited to the case where the whole modified region is located on the front face 3 side from the center position in the thickness direction of the semiconductor substrate 1.

The step of grinding the semiconductor substrate 1 will now be explained with reference to Figs. 17 to 21. Figs. 17 to 21 are views for explaining respective steps including the step of grinding the semiconductor substrate. In Example 1, the semiconductor substrate 1 is thinned from the thickness

FP03-0042-00

of 350 μm to a thickness of 50 μm .

As shown in Fig. 17, a protective film 20 is attached to the front face 3 of the semiconductor substrate after the starting point region for cutting is formed. The protective film 20 is used for protecting the functional devices 19 formed on the front face 3 of the semiconductor substrate 1 and holding the semiconductor substrate 1. Subsequently, as shown in Fig. 18, the rear face 21 of the semiconductor substrate 1 is subjected to surface grinding and then chemical etching, whereby the semiconductor substrate 1 is thinned to the thickness of 50 μm . As a consequence, i.e., because of the grinding of the rear face 21 of the semiconductor substrate 1, the rear face 21 reaches the fractures 15 generated from the starting point region for cutting acting as a start point, whereby the semiconductor substrate 1 is divided into semiconductor chips 25 having the respective functional devices 19. Examples of the chemical etching include wet etching ($\text{HF}\cdot\text{HNO}_3$) and plasma etching ($\text{HBr}\cdot\text{Cl}_2$).

Then, as shown in Fig. 19, an expansion film 23 is attached so as to cover the rear faces of all the semiconductor chips 25. Thereafter, as shown in Fig. 20, the protective film 20 attached so as to cover the functional devices of all the semiconductor chips 25 are peeled off. Subsequently, as shown in Fig. 21, the expansion film 23 is expanded, so that the semiconductor chips 25 are separated from each other,

FP03-0042-00

and a suction collet 27 picks up the semiconductor chips 25.

As explained in the foregoing, the substrate dividing method in accordance with Example 1 can grind the rear face 21 of the semiconductor substrate 1 after forming the functional devices 19 on the front face 3 of the semiconductor substrate 1 in the device manufacturing process. Also, because of the following effects respectively exhibited by the step of forming a starting point region for cutting and the step of grinding the semiconductor substrate, the semiconductor chips 25 thinned so as to respond to the smaller size of semiconductor devices can be obtained with a favorable yield.

Namely, the step of forming a starting point region for cutting can prevent unnecessary fractures and melting deviated from a desirable line along which the substrate should be cut for cutting the semiconductor substrate 1 from occurring, and thus can keep unnecessary fractures and melting from occurring in the semiconductor chips 25 obtained by dividing the semiconductor substrate 1.

The step of forming a starting point region for cutting does not melt the front face 3 of the semiconductor substrate 1 along the line along which the substrate should be cut, and thus can narrow the gap between the functional devices 19 adjacent each other, thereby making it possible to increase the number of semiconductor chips 25 separated from one

FP03-0042-00

semiconductor substrate 1.

On the other hand, the step of grinding the semiconductor substrate subjects the rear face 21 of the semiconductor substrate 1 to surface grinding such that the semiconductor substrate 1 attains a predetermined thickness after the starting point region for cutting is formed within the semiconductor substrate 1. Here, even if the rear face 21 reaches the fractures 15 generated from the starting point region for cutting acting as a start point, the cut surfaces of the semiconductor substrate 1 cut by the fractures 15 are in close contact with each other, whereby the semiconductor substrate 1 can be prevented from chipping and cracking because of the surface grinding. Therefore, the semiconductor substrate 1 can be made thinner and divided while preventing the chipping and cracking from occurring.

The close contact of the cut surfaces in the semiconductor substrate 1 is also effective in preventing the grinding dust caused by the surface grinding from entering the fractures 15, and keeping the semiconductor chips 25 obtained by dividing the semiconductor substrate 1 from being contaminated with the grinding dust. Similarly, the close contact of the cut surfaces in the semiconductor substrate 1 is effective in reducing the chip-off of the semiconductor chips 25 caused by the surface grinding as compared with the case where the semiconductor chips 25 are separated from each other. Namely, as the protective film 20, one with a

FP03-0042-00

low holding power can be used.

Since the rear face 21 of the semiconductor substrate 1 is subjected to chemical etching, the rear faces of the semiconductor chips 25 obtained by dividing the semiconductor substrate 1 can be made smoother. Further, since the cut surfaces of the semiconductor substrate 1 caused by the fractures 15 generated from the starting point region for cutting acting as a start point are in close contact with each other, only edge parts of the cut surfaces on the rear face side are selectively etched as shown in Fig. 22, whereby chamfers 29 are formed. Therefore, the transverse rupture strength of the semiconductor chips 25 obtained by dividing the semiconductor substrate 1 can be improved, and the chipping and cracking in the semiconductor chips 25 can be prevented from occurring.

The relationship between the semiconductor chip 25 and the molten processed region 13 after the step of grinding the semiconductor substrate includes those shown in Figs. 23A to 25B. The semiconductor chips 25 shown in these drawings have their respective effects explained later, and thus can be used according to various purposes. Figs. 23A, 24A, and 25A show the case where the fracture 15 reaches the front face 3 of the semiconductor substrate 1 before the step of grinding the semiconductor substrate, whereas Figs. 23B, 24B, and 25B show the case where the fracture 15 does not reach the front face 3 of the semiconductor

FP03-0042-00

substrate 1 before the step of grinding the semiconductor substrate. Even in the case of Figs. 23B, 24B, and 25B, the fracture 15 reaches the front face 3 of the semiconductor substrate 1 after the step of grinding the semiconductor substrate.

In the semiconductor chip 25 having the molten processed region 13 remaining within the cut surface as shown in Figs. 23A and 23B, the cut surface is protected by the molten processed region 13, whereby the transverse rupture strength of the semiconductor chip 25 improves.

The semiconductor chip 25 in which the molten processed region 13 does not remain within the cut surface as shown in Figs. 24A and 24B is effective in the case where the molten processed region 13 does not favorably influence the semiconductor device.

In the semiconductor chip 25 in which the molten processed region 13 remains in an edge part on the rear face side of the cut surface as shown in Figs. 25A and 25B, the edge part is protected by the molten processed region 13, whereby the chipping and cracking in the edge part can be prevented from occurring as in the case where the edge part of the semiconductor chip 25 is chamfered.

The rectilinearity of the cut surface obtained after the step of grinding the semiconductor substrate improves more in the case where the fracture 15 does not reach the front face 3 of the semiconductor substrate 1 before the

FP03-0042-00

step of grinding the semiconductor substrate as shown in Figs. 23B, 24B, and 25B than in the case where the fracture 15 reaches the front face 3 of the semiconductor substrate 1 before the step of grinding the semiconductor substrate as shown in Figs. 23A, 24A, and 25A.

Whether the fracture reaches the front face 3 of the semiconductor substrate 1 or not depends on not only the depth of the molten processed region 13 from the front face 3, but also the size of the molten processed region 13. Namely, when the molten processed region 13 is made smaller, the fracture 15 does not reach the front face 3 of the semiconductor substrate 1 even if the depth of the molten processed region 13 from the front face 3 is small. The size of the molten processed region 13 can be controlled by the output of the pulse laser light in the step of forming a starting point region for cutting, for example, and becomes greater and smaller as the output of the pulse laser light is higher and lower, respectively.

In view of a predetermined thickness of the semiconductor substrate 1 thinned in the step of grinding the semiconductor substrate, it is preferred that marginal parts (outer peripheral parts) of the semiconductor substrate 1 be rounded by at least the predetermined thickness by chamfering beforehand (e.g., before the step of forming a starting point region for cutting). Figs. 26A and 26B are respective sectional views of a marginal part of the

FP03-0042-00

semiconductor substrate 1 before and after the step of grinding the semiconductor substrate in accordance with Example 1. The thickness of the semiconductor 1 shown in Fig. 26A before the step of grinding the semiconductor substrate is 350 μm , whereas the thickness of the semiconductor 1 shown in Fig. 26B after the step of grinding the semiconductor substrate is 50 μm . As shown in Fig. 26A, a plurality of (seven here) rounded portions are formed at the marginal part of the semiconductor substrate 1 beforehand by chamfering with a thickness of 50 μm each, i.e., the marginal part of the semiconductor substrate 1 is caused to have a wavy form. As a consequence, the marginal part of the semiconductor substrate 1 after the step of grinding the semiconductor substrate 1 attains a state rounded by chamfering as shown in Fig. 26B, whereby the chipping and cracking can be prevented from occurring at the marginal part, and handling can be made easier because of an improvement in mechanical strength.

Example 2

Example 2 of the substrate dividing method in accordance with the present invention will now be explained with reference to Figs. 27 to 35. Example 2 relates to a case where the substrate 1 is a sapphire substrate (having a thickness of 450 μm and an outer diameter of 2 inches) which is an insulating substrate ("substrate 1" will hereinafter be referred to as "sapphire substrate 1" in Example 2), so

FP03-0042-00

as to yield a semiconductor chip to become a light-emitting diode. Figs. 28 to 35 are sectional views of the sapphire substrate 1 taken along the line XX-XX of Fig. 27.

5 First, as shown in Fig. 28, the sapphire substrate 1 is irradiated with laser light L while a light-converging point P is positioned therewithin, so as to form a modified region 7 within the sapphire substrate 1. In a later step, a plurality of functional devices 19 are formed like a matrix on the front face 3 of the sapphire substrate 1, and the
10 sapphire substrate 1 is divided into the functional devices 19. Therefore, lines to cut are formed like a grid in conformity to the size of each functional device 19 as seen from the front face 3 side, modified regions 7 are formed along the lines to cut, and the modified regions 7 are used
15 as starting point regions for cutting.

When the sapphire substrate 1 is irradiated with laser light under a condition with an electric field intensity of at least 1×10^8 (W/cm²) at the light-converging point P and a pulse width of 1 μ s or less, a crack region is formed
20 as the modified region 7 (there is also a case where a molten processed region is formed). When the (0001) plane of the sapphire substrate 1 is employed as the front face 3, and a modified region 7 is formed in a direction along the (1120) plane and a direction orthogonal thereto, the substrate can
25 be cut by a smaller force with a favorable accuracy from the starting point region for cutting formed by the modified

FP03-0042-00

region 7 as a start point. The same holds when a modified region 7 is formed in a direction along the (1100) plane and a direction orthogonal thereto.

5 After the starting point region for cutting is formed by the modified region 7, an n-type gallium nitride compound semiconductor layer (hereinafter referred to as "n-type layer") 31 is grown as a crystal until its thickness becomes 6 μm on the front face 3 of the sapphire substrate 1, and a p-type gallium nitride compound semiconductor layer
10 (hereinafter referred to as "p-type layer") 32 is grown as a crystal until its thickness becomes 1 μm on the n-type layer 31. Then, the n-type layer 31 and p-type layer 32 are etched to the middle of the n-type layer 31 along the modified regions 7 formed like a grid, so as to form a plurality of
15 functional devices 19 made of the n-type layer 31 and p-type layer 32 into a matrix.

After the n-type layer 31 and p-type layer 32 are formed on the front face 3 of the sapphire substrate 1, the sapphire substrate 1 may be irradiated with laser light L while the
20 light-converging point P is positioned therewithin, so as to form the modified regions 7 within the sapphire substrate 1. The sapphire substrate 1 may be irradiated with the laser light L from the front face 3 side or rear face 21 side. Even when the laser light L is irradiated from the front
25 face 3 side after the n-type layer 31 and p-type layer 32 are formed, the n-type layer 31 and p-type layer 32 can be

FP03-0042-00

prevented from melting, since the laser light L is transmitted through the sapphire substrate 1, n-type layer 31, and p-type layer 32.

After the functional devices 19 made of the n-type layer 31 and p-type layer 32 are formed, a protective film 20 is attached to the front face 3 side of the sapphire substrate 1. The protective film 20 is used for protecting the functional devices 19 formed on the front face 3 of the semiconductor substrate 1 and holding the sapphire substrate 1. Subsequently, as shown in Fig. 31, the rear face 21 of the sapphire substrate 1 is subjected to surface grinding, so that the sapphire substrate 1 is thinned to the thickness of 150 μm . The grinding of the rear face 21 of the sapphire substrate 1 generates a fracture 15 from a starting point region for cutting formed by the modified region 7 as a start point. This fracture 15 reaches the front face 3 and rear face 21 of the sapphire substrate 1, whereby the sapphire substrate 1 is divided into semiconductor chips 35 each having the functional device 19 constituted by the n-type layer 31 and p-type layer 32.

Next, an expandable expansion film 23 is attached so as to cover the rear faces of all the semiconductor chips 25 as shown in Fig. 32, and then the protective film 20 is irradiated with UV rays as shown in Fig. 33, so as to cure a UV curable resin which is an adhesive layer of the protective film 20. Thereafter, the protective film 20 is peeled off

FP03-0042-00

as shown in Fig. 34. Subsequently, as shown in Fig. 35, the expansion film 23 is expanded outward, so as to separate the semiconductor chips 25 from each other, and the semiconductor chips 25 are picked up by a suction collet
5 or the like. Thereafter, electrodes are attached to the n-type layer 31 and p-type layer 32 of the semiconductor chip 25, so as to make a light-emitting diode.

In the step of forming a starting point region for cutting in the substrate dividing method in accordance with
10 Example 2, as explained in the foregoing, the sapphire substrate 1 is irradiated with the laser light L while the light-converging point P is positioned therewithin, so as to form a modified region 7 by generating a phenomenon of multiphoton absorption, whereby the modified region 7 can
15 form a starting point region for cutting within the sapphire substrate 1 along a desirable line along which the substrate should be cut for cutting the sapphire substrate 1. When a starting point region for cutting is formed within the sapphire substrate 1, a fracture 15 is generated in the
20 thickness direction of the sapphire substrate 1 from the starting point region for cutting acting as a start point naturally or with a relatively small force exerted thereon.

In the step of grinding the sapphire substrate 1, the sapphire substrate 1 is ground so as to attain a predetermined
25 thickness after a starting point region for cutting is formed within the sapphire substrate 1. Here, even when the ground

FP03-0042-00

surface reaches the fracture 15 generated from the starting point region for cutting acting as a start point, the cut surfaces of the sapphire substrate 1 cut by the fracture 15 are in close contact with each other, whereby the sapphire
5 substrate 1 can be prevented from chipping and cracking upon grinding.

Therefore, the sapphire substrate 1 can be thinned and divided while preventing the chipping and cracking from occurring, whereby semiconductor chips 25 with the thinned
10 sapphire substrate 1 can be obtained with a favorable yield.

Effects similar to those mentioned above are also obtained when dividing a substrate using an AlN substrate or GaAs substrate instead of the sapphire substrate 1.

Industrial Applicability

15 As explained in the foregoing, the present invention can thin and divide the substrate while preventing the chipping and cracking from occurring.